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APPLICATION NO.	FILING DATE	ING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET		CONFIRMATION NO.
10/073,328	02/13/2002	Takao Hasegawa	401558	4562

TITLE OF INVENTION: METHOD OF WIRING SEMICONDUCTOR INTEGRATED CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT, AND COMPUTER PRODUCT

APPLN. TYPE	SMALL ENTITY	ISSUE FE	EE	PUBLICATION FEE	TOTAL F	EE(S) DUE	DATE DUE
nonprovisional	nonprovisional NO			\$300		630	07/02/2004
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GARBOWS	KI, LEIGH M	2825		716-012000	-		
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RENESAS TECHNOLOGY CORP. TY	TOKYO, JAPAN
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